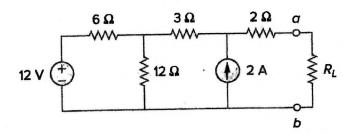
5/3

Problem: #1

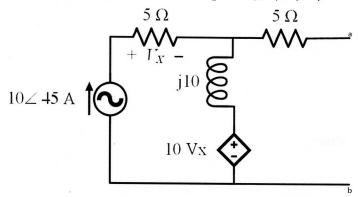
Area: Circuit I

Student Code\_\_\_\_

Referring to the circuit below, use source transformation method to arrive at the Thevenin voltage and resistance, between terminals a and b, first and then determine the value of R<sub>L</sub> for maximize the power delivered to the entire load circuit. Evaluate the maximum power.



For the following circuit, answer parts a), b), c), d).



- a) Find the open circuit voltage,  $V_{OC}$ , or short circuit current,  $I_{SC}$ , at the terminals a-b.
- b) Using a  $1 \ge 0$  dummy voltage source at the terminals a-b, find the Thevenin equivalent impedance  $Z_{Th}$ .
- c) Find the Norton Current, I<sub>N</sub>, and the Thevenin Voltage, V<sub>Th</sub>.
- d) Draw the equivalent Thevenin and Norton circuits.

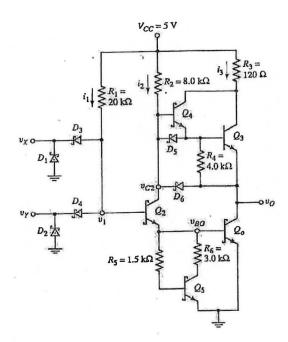
## Problem: 4

## **Area Electronics**

Student Code\_\_\_\_

Consider the following low power Schottky (TTL) circuit. Assume that the schottky diode turn-on voltage is  $V_{\gamma}$  (SD) = 0.3 V and the transistor parameters are :  $V_{BE}$  (on) = 0.7 V,  $V_{CE}$  (sat) = 0.4 V and  $\beta$  = 25. Calculate power dissipation for the low input,  $P_L$  and high input,  $P_H$  conditions.

Assume that for the low input condition,  $v_X = v_Y = 0.4 \ V$  and for high input condition,  $v_X = v_Y = 3.6 \ V$ 



Refer to Fig. 1 for the system with ideal Continuous-to-Discrete (C-to-D) and Discrete-to-Continuous (D-to-C) converters.

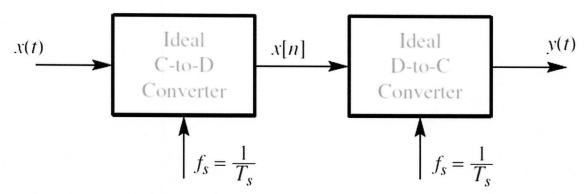


Figure 1: Ideal C-to-D and D-to-C system.

- (a) Suppose that the output from the C-to-D converter is  $x[n] = \cos(0.2\pi n)$ , and the sampling rate of the C-to-D converter is  $f_s = 8000$  samples/s. Determine a formula for the continuous-time sinusoidal input x(t) using the smallest frequency greater than 10000 Hz.
- (b) Suppose the output from the C-to-D converter is  $x[n] = \cos(0.25\pi n)$ , the input signal is  $x(t) = \cos(510\pi t)$ , and the sampling rate (fs) of the C-to-D converter is less than 130 samples/s. Determine the largest possible sampling rate satisfying these three conditions.

**Area: Communications / Signal Processing** 

Student Code: \_\_\_\_\_

Let a be a random variable which is uniformly distributed on the interval [0,2]. Define two random variables X and Y as

$$X \triangleq \min \{a, 2 - a\},\$$
  
 $Y \triangleq \max \{a, 2 - a\},\$ 

and let  $Z \triangleq \frac{Y}{X}$ .

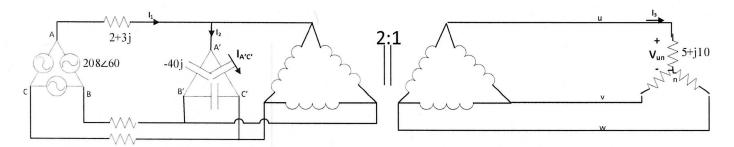
- (1) Please derive the probability density function of X.
- (2) Please derive the probability density function of Z.
- (3) Please calculate the expectation of  $\frac{x}{y}$ .

Area: Power

Student Code: \_\_\_\_\_

In the following circuit, find the following (both amplitude and angle):

- a)  $I_1$ ,  $I_2$ , and  $I_3$  (45 points).
- b) I<sub>A'C'</sub> (Capacitor current) (15 points).
- c) Phase voltage of the load (5+10j) equal to  $V_{un}$  (15 points).
- d) 3-phase complex power of the source (15 points)
- e) 3-phase complex power of the capacitor bank. (10 points)

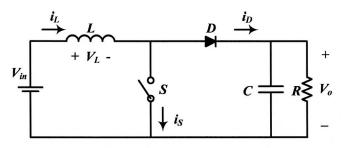


Problem: 154

Area: Power

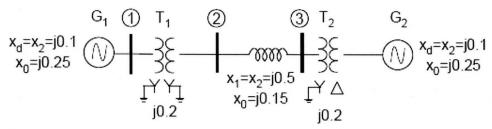
Student Code:\_\_\_\_

A Boost dc-dc converter has the following parameters:  $V_{in} = 400 \text{ V}$ , d = 0.25,  $V_{out} = 650 \text{ V}$ ,  $L = 100 \text{ }\mu\text{H}$ , and  $f_{sw} = 100 \text{ }k\text{Hz}$ .



- a) Find the peak value of the inductor current.
- b) Accurately plot the waveform of the inductor current.
- c) Find the average value of the inductor current.
- d) Find the input power.
- e) Find the value of the load resistor.

In the system shown below, a solid single line-ground fault occurs on bus 3. Both generators are solidly grounded.



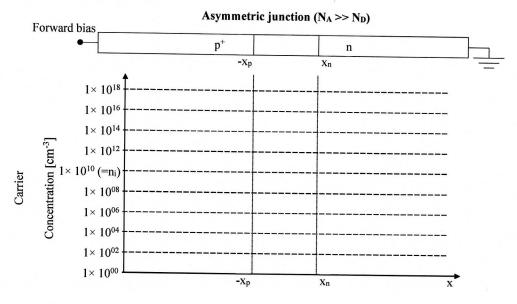
The prefault voltages are 1.0 per unit. What are the positive, negative and zero sequence fault currents in line 2-3 during the fault?

At 300 K, an asymmetric abrupt p-n junction's acceptor concentration ( $N_A$ ) is  $1.0 \times 10^{18}$  cm<sup>-3</sup> in the p-side and donor concertation ( $N_D$ ) of  $1.0 \times 10^{12}$  cm<sup>-3</sup> in the n-side. Calculate the equilibrium minority carrier concentrations in p-side and n-side:

p-side:

n-side:

Complete two plots of  $\Delta p(x)$  and  $\Delta n(x)$  profiles across this junction when  $\Delta p(x=x_n) = 1.0 \times 10^{10}$  cm<sup>-3</sup> and  $\Delta n(x=-x_p) = 1.0 \times 10^{10}$  cm<sup>-3</sup> under forward bias.



Constants*	Equations (p-n junction)*
kT = 0.0259 [eV] (at 300 K)	
Si Bandgap = $1.12$ [eV] (at $300$	
<u>K</u> )	and the property of the property and
Intrinsic carrier concentration of $Si = 1 \times 10^{10} \text{ [cm}^{-3}\text{]}$ (at 300 K)	$p_{no} (= p_{po}) = N_v \exp[-(E_F - E_V) / kT]$
$N_c = 2.86 \times 10^{19} \text{ [cm}^{-3]}$ (at 300 K)	
$N_v = 2.66 \times 10^{19} \text{ [cm}^{-3]}$ (at 300 K)	
Elementary charge = $1.6 \times 10^{-19}$ [C]	$p_{no} (= p_{po}) = n_i \exp[(E_i - E_F) / kT]$
$\varepsilon_0 = 8.85E-14$ [F/cm]	$n_{\text{no}}p_{\text{no}} = n_{\text{i}}^2$
$\varepsilon_{\rm s} = 11.9 \ \varepsilon_{\rm o} \ [\text{F/cm}]$ (Si)	$n_{po}p_{po}=n_i^2$
$\varepsilon_{\text{ox}} = 3.9 \ \varepsilon_{\text{o}}  \text{[F/cm]}$ (SiO <sub>2</sub> )	$n_n = n_{no} + \Delta n$ , $p_n = p_{no} + \Delta p$ ( $\Delta n = \Delta p$ )
	$n_p = n_{po} + \Delta n$ , $p_p = p_{po} + \Delta p$ ( $\Delta n = \Delta p$ )

<sup>\*</sup> Definition of parameters are not provided. It is expected that the examinee interprets the meaning.

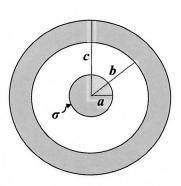
Problem: 19

**Area: Waves & Devices** 

Student Code: \_\_\_\_

Concentric spherical metal shell. The figure below shows a cross-section of a concentric spherical metal shell. The radii of the inner solid spherical conductor, the inner wall of the outer spherical conductor, and the outer wall of the outer spherical conductor are a, b, and c, respectively, as marked in the figure. Consider a positive surface charge density  $\sigma$  at the surface of the inner spherical conductor.

- A) Find the electric field everywhere as a function of radial distance r (i.e., r < a, a < r < b, b < r < c and r > c).
- B) Please plot the electric field strength as a function of radial distance r.



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**Area: Waves & Devices** 

Student	Codo	
Juueni	Code:	

Air-perfect conductor interface. A uniform plane electromagnetic wave traveling in the air with its electric field given by  $\overline{E_i(x,t)} = \hat{y}15\cos\left(44\times10^9t - \beta x\right)$  V/m is normally incident on a perfect conductor boundary located at x=0. (a) Find the propagation constant  $\beta$ . (b) Find the corresponding magnetic field  $\overline{H_i(x,t)}$ . (c) Find the electric and magnetic fields  $\overline{E_r(x,t)}$  and  $\overline{H_r(x,t)}$  of the reflected wave. (d) Find the nearest two positions in the air away from the boundary where the total electric field is always zero.

Problem:#2	Area:	Computational	Intelligence
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Code	#
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Answer the questions for parts a and b below.

(a) Explain the validity that a given CI technique can be applied to any application that requires classification of a dataset into known classes. Provide examples where possible.

- (b) Define the following terms:
  - (i) Data fusion

(ii) artificial intelligence

Answer the questions for parts a and b below.

(a) Describe different types of hardware evolution. Provide diagrams where possible.

- (b) Define the following terms:
  - (i) Embryonics

(ii) Adaptive Devices, Circuits and Systems

(iii) Immunotronics

Given the following feedforward neural network in Fig. Q2 with a sigmoid function (given below) in the hidden layer and a linear function in the output layer

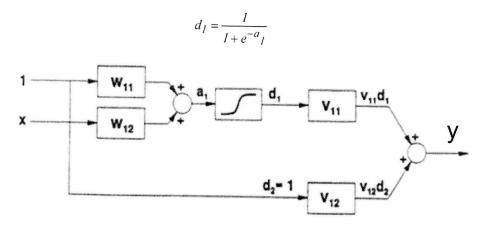


Fig. Q2

Given that  $[W_{11}, W_{12}, V_{11}, V_{12}] = [1.0, 0.5, -1, 0.5]$ , calculate the output y for x = 0.5.

(a) Are neural networks, as currently used in engineering, based on models of the human brain? Explain the reasons for your answer in detail.

(b) In the context of computational intelligence algorithms, define the following terms: (i) Supervised learning. (ii) Unsupervised learning. (iii) Reinforcement learning.

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Area: Integrated Circuits and Logic Design

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Answer the questions for parts  ${\bf a}$  and  ${\bf b}$  below.

a. Simplify the logic expression  $G(a,b,c) = (ab+c)(\overline{bc+a})(\overline{a+c})$  using any appropriate method to obtain the minimal sum-of-products (SOP) expression. Individual gates are not restricted on the number of inputs. Show your work for full credit.

b. Draw the logic network (digital circuit) for the minimal SOP from part a. The logic gates are not restricted with the number of inputs.

Area: Integrated Circuits and Logic Design

Code #\_\_\_\_

Answer the following questions.

a) Convert to binary: 9CD (hex) =

b) Convert to binary: 179.3125 (decimal)

c) Convert to octal: 1110110110010.1110110 (binary) = d) Convert –61 to 8 bit 2's complement representation.

e) Perform the 2's complement multiplication 11001 x 11010

f) Perform the following two's complement addition operations. Give your answer as an 8-bit two's complement number. State whether overflow occurs.

11101110

01010110

+10111011

+00110011

PAGE 2 OF 2

Area: Integrated Circuits and Logic Design

Code #	
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Design a 2-bit up counter with the state sequence specified as 01230123... using SR flip-flops, including drawing the counter circuit. Show your design steps for full credit.

Area: Integrated Circuits and Logic Design

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Draw a circuit to implement the function  $g(w,x,y,z) = \sum m(1,3,4,5,8,9,10,11,13,15)$  using two 3-to-8 decoders with active low outputs and active high enables. You may use other logic gates as needed for the implementation of the decoder circuit.

W	X	Y	Z	g
W 0	0	0	0	-
0	0	0	1	
0 0 0 0	0	1	0	
0	0	1	1	
0	1	0		
0	1	0	0	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

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rea: Networking, Security, and Dependability

Student	Codo
Student	Code:

This problem has two parts. For full credit, you must answer both parts correctly.

- a. Compare the UDP and TCP protocols in terms of each of the following attributes:
  - bit overhead
  - processing overhead
  - reliability
  - flexibility in supporting different type of traffic, e.g, VoIP, file transfer, telemetry

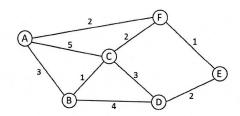
b. Compare the functions of a hub/repeater, switch, and router in computer networks. Explain which type of node is required when connecting ATM-based and Ethernet-based networks in the TCP/IP stack.

Area: Networking, Security, and Dependability

Student Code: \_\_\_\_

This problem has two parts. For full credit, you must answer both parts correctly.

Answer the following questions for the network shown below:



a. Suppose each node runs the Distance Vector Routing Protocol. Complete the table below to show, step-by-step, how to find the shortest path from each node to destination node F. Each entry in columns A through E should be of the form (NextHop, Cost).

Iteration	A	В	С	D	Е

b. Suppose the link between nodes F and E is broken (removed) after the protocol converges in part a. Show step by step how the protocol continues until it converges again.

Iteration	Α	В	С	D	Е
			-		

This problem has two parts. For full credit, you must answer both parts correctly.

a. Nodes A and B are attached to opposite ends of a 100 Mbps Ethernet cable segment. Suppose A begins transmitting a frame and before it finishes, B begins transmitting a frame. For A to detect collision with B, what should be the maximum length of the Ethernet cable segment? Assume that the transmit frame size is 500 bits and signal propagation speed is 2 x 108 m/sec. Show your work.

b. The table below is a routing table using CIDR. Address bytes are in hexadecimal. Leading zeroes are dropped for each byte, e.g., 02 is written as 2. The notation "/12" in C4.50.0.0/12 denotes a mask with 12 leading 1 bits: FF.F0.0.0.

Prefix/Length	Next Hop
C4.50.0.0/12	Α
C4.5E.10.0/20	В
C4.60.0.0/12	С
C4.68.0.0/14	D .
80.0.0.0/1	Е
40.0.0.0/2	F
3.0.0.0/2	G

Determine the next hop for a packet with each of the following destination addresses, assuming the match with the longest prefix is selected. Show your work.

- i. C4.6B.31.2E
- ii. 5E.43.91.12

Problem: P36	Area: Networking,	Security,	and Dependability
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Student	Cada
Student	Code:

This problem has two parts. For full credit, you must answer both parts correctly.

Consider a three-person group encryption scheme based on the same principle as RSA. Suppose that some trusted entity generates two primes, p and q and forms n=pq. Now, instead of choosing a public key, e, and private key, d (as in RSA), the trusted entity chooses  $k_1$ ,  $k_2$ , and  $k_3$  such that  $\gcd(k_j, n)=1$  and  $k_1k_2k_3=1$  mod  $\phi(n)$ .

The three users A, B, and C, respectively, are given the following keys.

A gets  $(k_1; k_2; n)$  B gets  $(k_2; k_3; n)$  C gets  $(k_1; k_3; n)$ 

a. Suppose user A generates a message m such that  $\gcd(m,n)=1$ . A wants to encrypt m so that both B and C can decrypt the ciphertext. To this end, A forms the ciphertext  $y=m^{k_1k_2} \mod n$ . Explain how B would decrypt y, and explain how C would decrypt y.

b. Suppose A and B have been collaborating on some class project and have produced the message m, where  $\gcd(m,n)=1$  again. They would like to collaborate to create a ciphertext that only C can decrypt, in a way that once m is encrypted, both A and B would have to work together to decrypt the resulting ciphertext to recover m; neither A nor B on their own can accomplish the decryption. Explain how this can be accomplished.